

FIG. 1

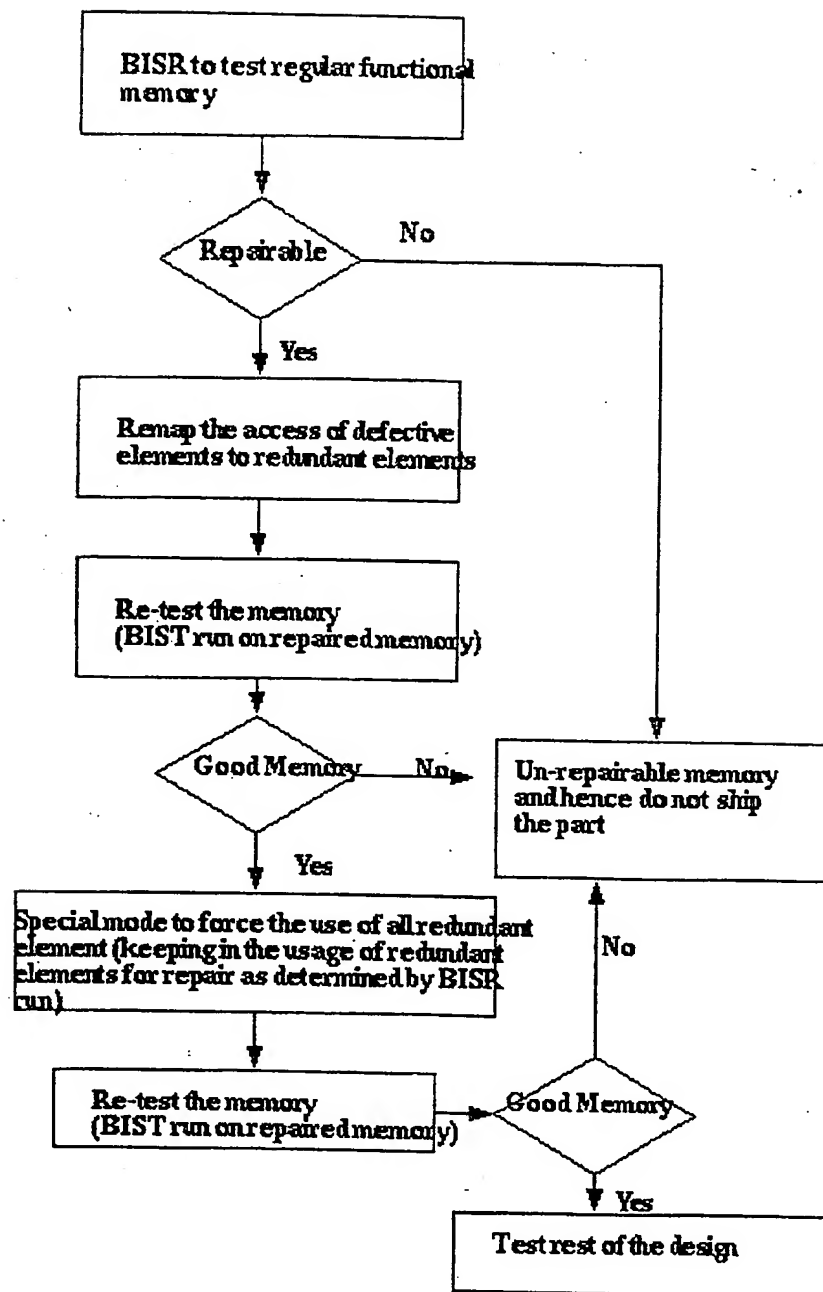


FIG. 2

BISR Mode

means for testing redundant elements
of the memory which are not used

100

means for checking interaction between
redundant elements of the memory which are
not used and adjacent functional memory

102

means for using repair information
to repair the memory

104

means for forcing usage of (possibly all)
redundant elements which are not needed to
be used for repairing the memory (such as by faking
defects to remap good elements with redundant elements)

106

FIG. 3